



Features

- 350 Watts Peak Pulse Power per Line ($t_p = 8/20\mu s$)
- Bidirectional Configuration
- Protects One Power or I/O Port
- ESD Protection > 40 kilovolts
- Low Clamping Voltages
- Ultra Low Capacitance: 1.0 pF Typical
- AEC-Q101 Qualified

IEC Compatibility (EN61000-4)

- IEC 61000-4-2 (ESD) $\pm 15kV$ (air), $\pm 8kV$ (contact)
- IEC 61000-4-4 (EFT) 40A (5/50ns)
- IEC 61000-4-5(Surge): 20A, 8/20 μs



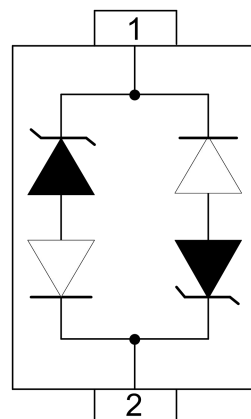
Mechanical Characteristics

- Molded JEDEC SOD-323 package
- Weight 10 milligrams (Approximate)
- Flammability rating UL 94V-0
- 8mm Tape and Reel Per EIA Standard 481
- Device Marking: Marking Code
- RoHS Compliant

Applications

- Ethernet - 10/100/1000 Base T
- Cellular Phones
- Handheld - Wireless Systems
- Personal Digital Assistant (PDA)
- USB Interface

PIN Configuration



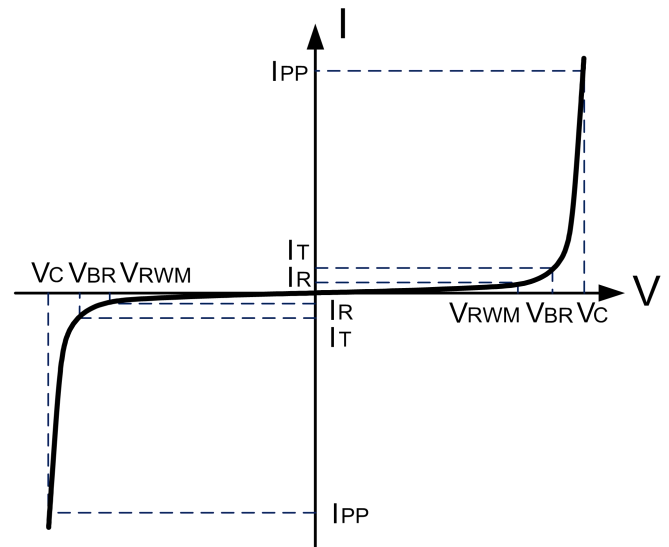
BIDIRECTIONAL

Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Power ($t_p=8/20\mu s$) - See Figure 1	P_{PP}	350	Watts
Operating Temperature	T_J	-55 to + 150	$^{\circ}C$
Storage Temperature	T_{STG}	-55 to +150	$^{\circ}C$

Electrical Parameters (T=25 $^{\circ}C$)

Symbol	Parameter
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
V_{RWM}	Working Peak Reverse Voltage
I_R	Maximum Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current
I_F	Forward Current
V_F	Forward Voltage @ I_F



Electrical characteristics

PART NUMBER (See Note 1 & Note 2)	DEVICE MARKING	RATED STAND-OFF VOLTAGE V_{WM} (Volts)	MINIMUM BREAKDOWN VOLTAGE @ 1mA V_{BR} (Volts)	MAXIMUM CLAMPING VOLTAGE (See Fig. 2) @ $I_P = 1A$ V_C (Volts)	MAXIMUM CLAMPING VOLTAGE (See Fig. 2) @ 8/20 μs V_C @ I_{PP}	MAXIMUM LEAKAGE CURRENT @ V_{WM} I_d (μA)	TYPICAL CAPACITANCE @ 0V, 1 MHz C(pF)
DW03DLC-B-S	3B	3.3	4.0	7.0	19.0V @ 20.0A	1	1

Note 1: Part numbers with an additional "B" suffix are bidirectional devices

Note 2: For Bidirectional Devices Only: Electrical characteristics apply in both directions.

Typical Characteristics

Figure 1: Peak Pulse Power vs. Pulse Time

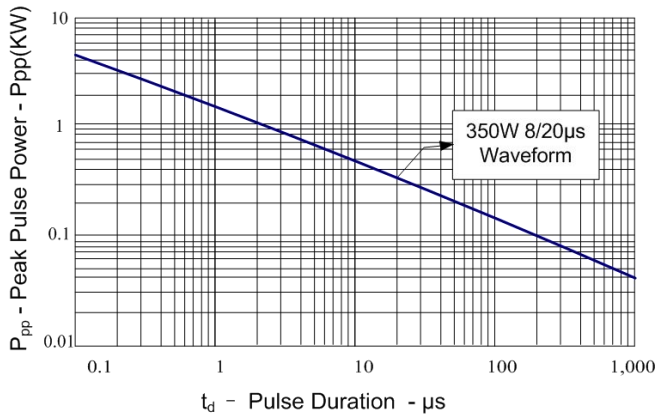


Figure 2: Power Derating Curve

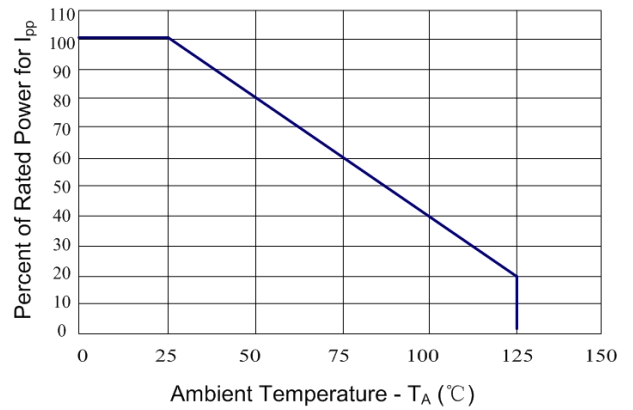


Figure 3: Clamping Voltage vs. Peak Pulse Current

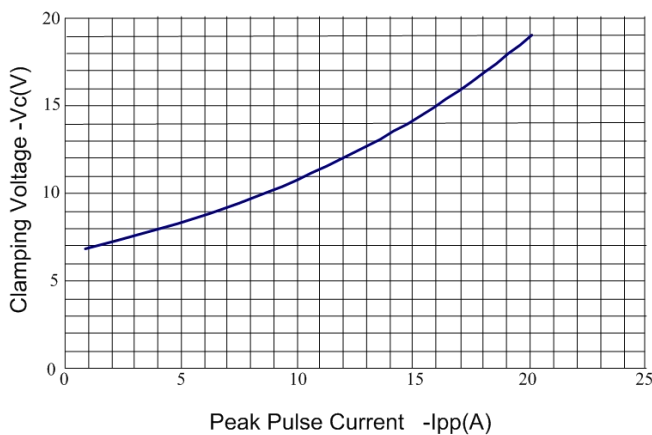


Figure 4: Normalized Junction Capacitance vs. Reverse Voltage

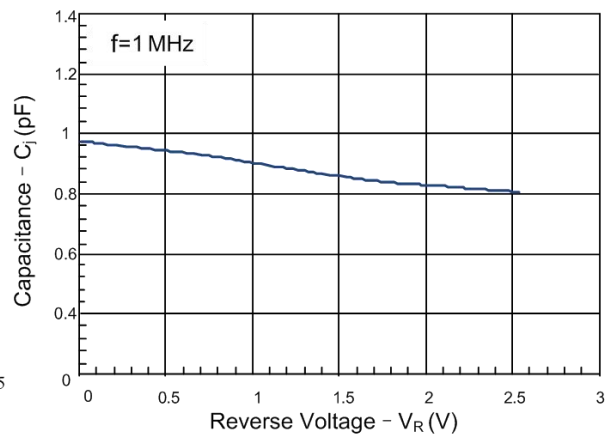


Figure 5: Pulse Waveform

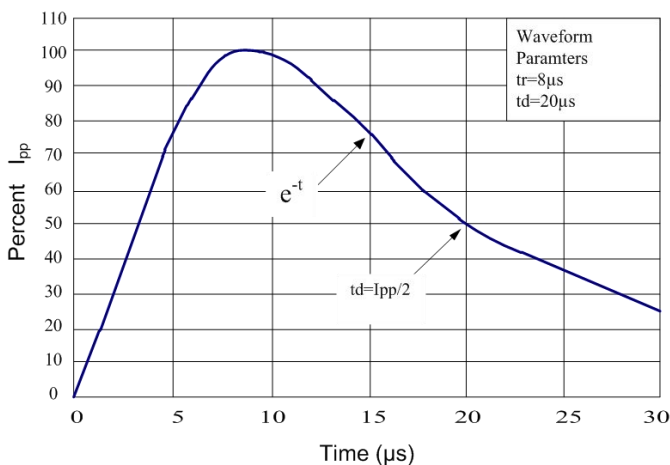
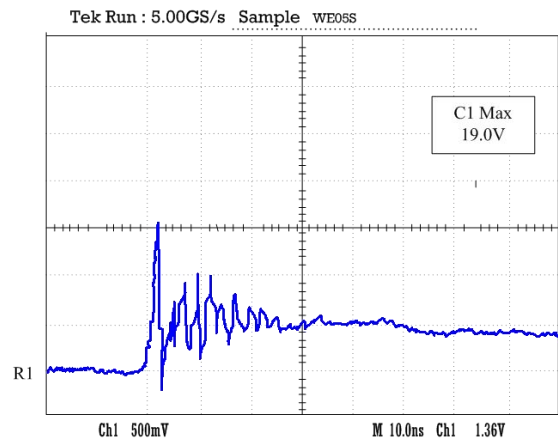
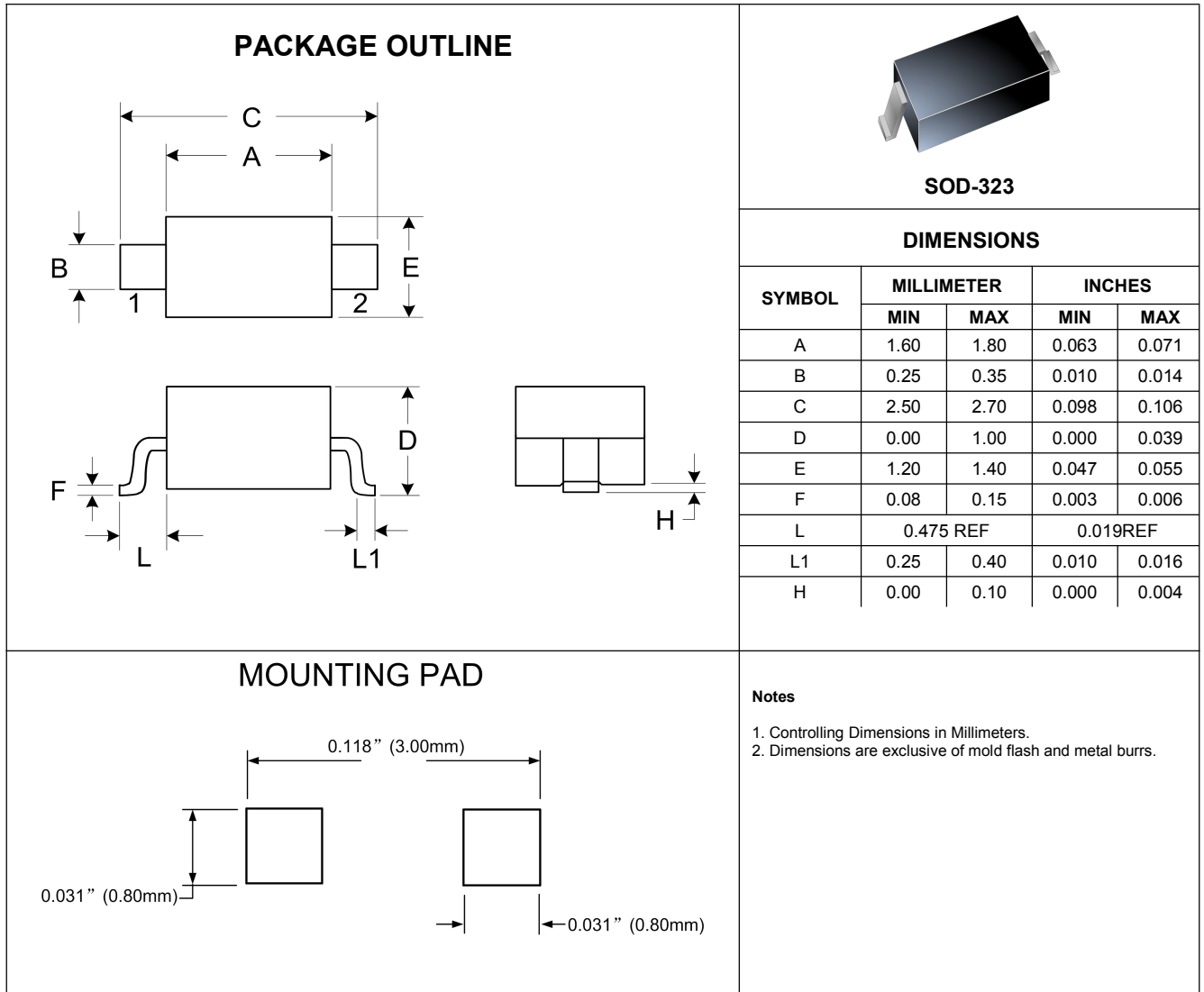


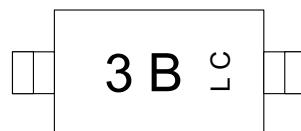
Figure 6: ESD Clamping (8kV Contact per IEC 61000-4-2)



Outline Drawing – SOD323



Marking Codes



Package Information

Qty: 3k/Reel